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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/769,417	01/26/2001	Yoshio Fudeyasu	49657-962 4245	
7590 07/21/2004 McDERMOTT, WILL & EMERY 600 13th Street, N.W.			EXAMINER	
			NAMAZI, MEHDI	
Washington, DC 20005-3096			ART UNIT	PAPER NUMBER
			2188	16
			DATE MAILED: 07/21/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)
Office Action Comments	09/769,417	FUDEYASU, YOSHIO
Office Action Summary	Examiner	Art Unit
	Mehdi Namazi	2188
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	correspondence address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w. - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	66(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) day fill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 29 Ju	<u>ly 2003</u> .	
2a) This action is FINAL . 2b) ☑ This	action is non-final.	
3) Since this application is in condition for allowant closed in accordance with the practice under E	·	
Disposition of Claims		
 4) Claim(s) 3-17 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) Claim(s) 4-14 and 17 is/are allowed. 6) Claim(s) 3 and 15 is/are rejected. 7) Claim(s) 16 is/are objected to. 8) Claim(s) are subject to restriction and/or 		
Application Papers		
9)☐ The specification is objected to by the Examiner	•	
	epted or b) \square objected to by the E	
Applicant may not request that any objection to the c		
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Example 11.		
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	have been received. have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s)		
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	

Art Unit: 2188

DETAILED ACTION

1. Applicant's request during the interview on July 29, 2003 for reconsideration of the finality of the rejection of the last Office action is persuasive and, therefore, the finality of that action is withdrawn.

Claim Rejections - 35 USC § 102

- 2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:
 - (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 3, and 15 is rejected under 35 U.S.C. 102(b) as being anticipated by Motomura (U.S. Patent No. 6,338,108).

As per claim 3, Motomura teaches a semiconductor memory device comprising: A plurality of input terminals for receiving write data, a control signal and an address signal (fig. 2C, elements 24, and 25, address signal is not shown. It is inherent in Random Access Memory to have address signal because the memory is accessed randomly, not sequentially); and at least one output terminal (fig. 2C, element 26), different in number from the input terminals for receiving the write data, for outputting read data (2 input terminals to 1 output terminal), wherein the input terminals are coupled to a first bus (fig. 2C is the request bus, where input terminals are connected to), and said at least one output terminal is coupled to a second bus (fig. 2C, is response bus, wherein output terminal is connected to), and each of the first and

Application/Control Number: 09/769,417

Art Unit: 2188

second buses is a unidirectional bus for transferring a signal or data in one direction (col. 3, lines 35-38).

As per claim 15, Montomura teaches the input terminals receive the write data, the control signal and the address signal at common terminals (fig. 9C, element 5).

Allowable Subject Matter

- 4. Claims 4-14, and 17 are allowed.
- 5. Claim 16 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mehdi Namazi whose telephone number is 703-306-2758. The examiner can normally be reached on Monday-Friday 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 703-306-2903. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Page 3

Application/Control Number: 09/769,417

Art Unit: 2188

Page 4

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Mehdi Namazi

Examiner Art Unit 2188

July 8, 2004

MANO PADMANABHAN SUPERVISORY PATENT EXAMINER

Mans Romanashan 7/12/04: